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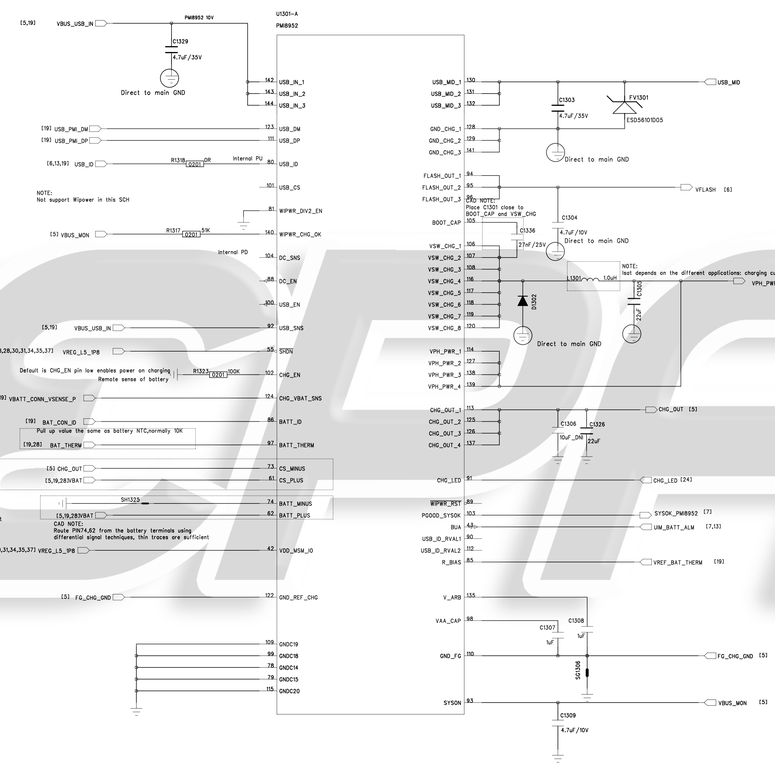

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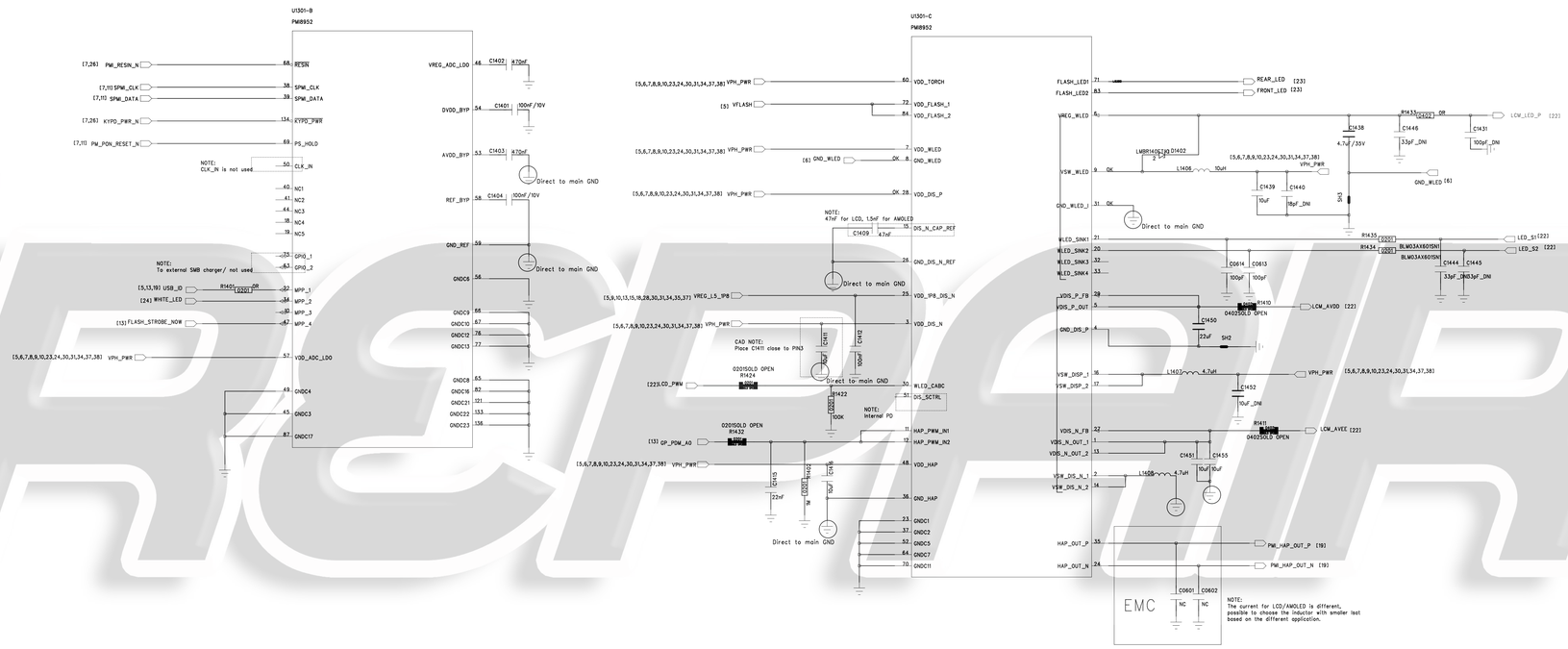

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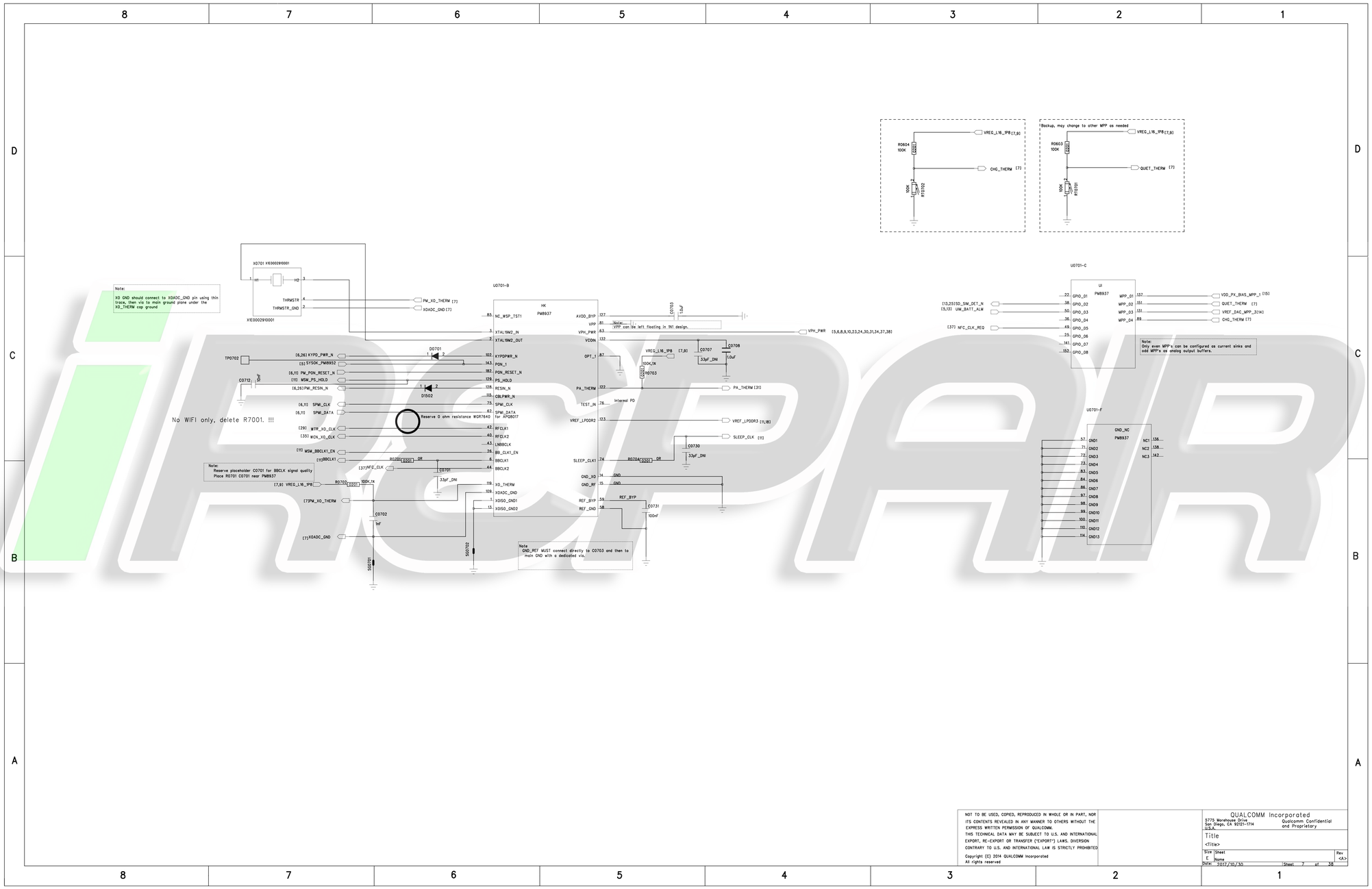

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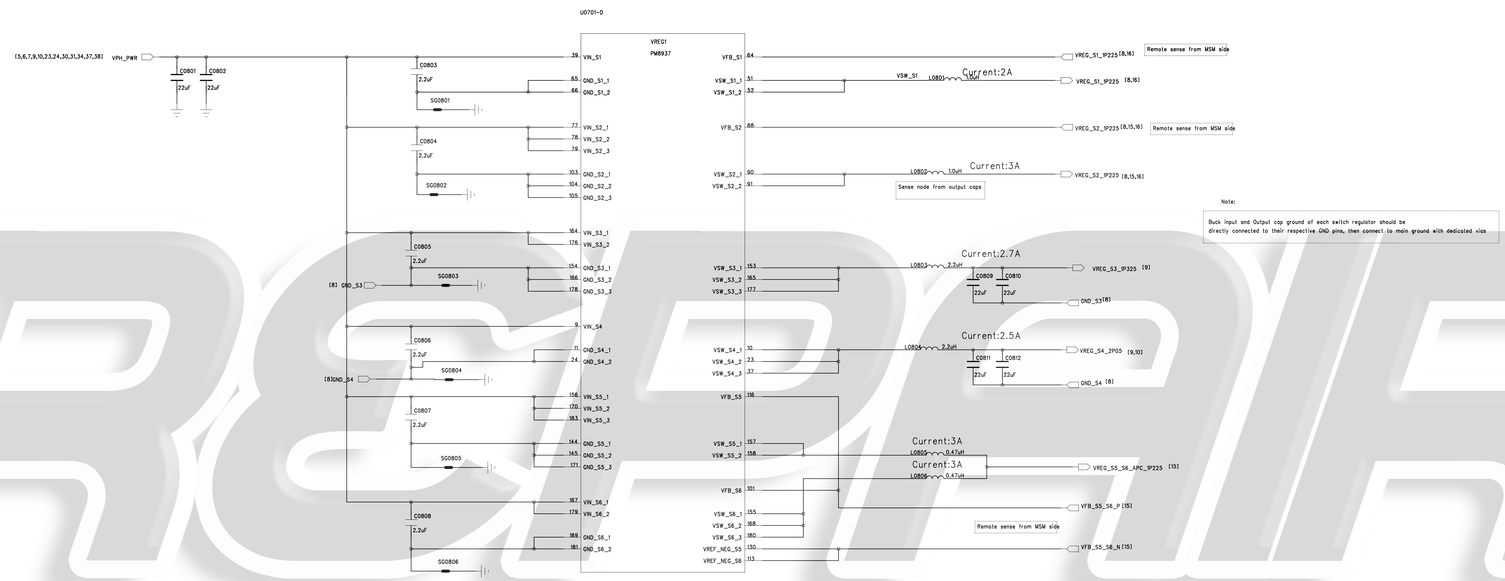

Note:

Buck input and Output cap ground of each switch regulator should be directly connected to their respective GND pins, then connect to main ground with dedicated vias

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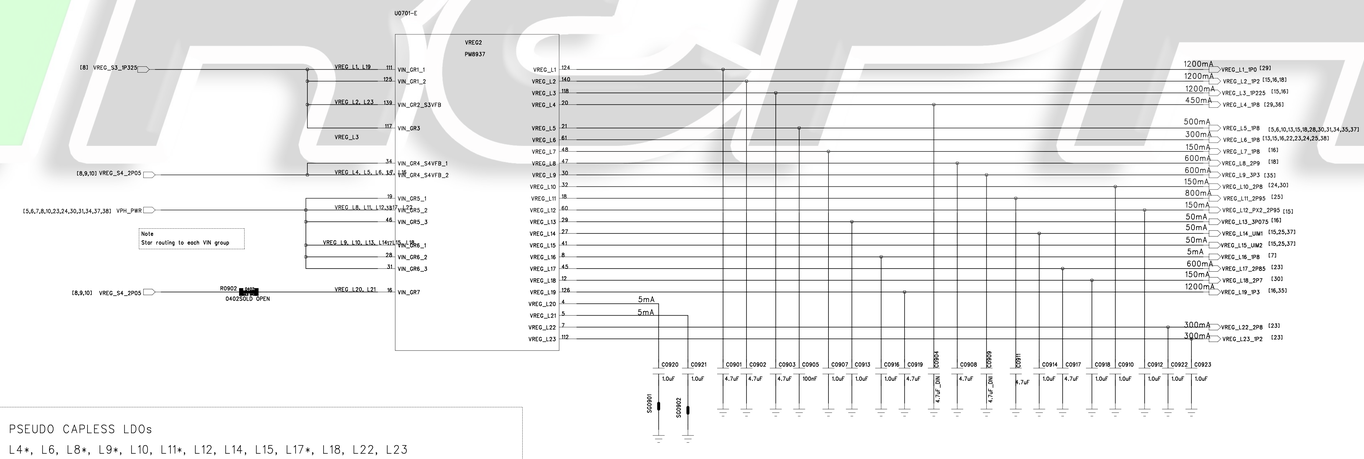

PSEUDO CAPLESS LDOs

L4*, L6, L8*, L9*, L10, L11*, L12, L14, L15, L17*, L18, L22, L23

L4*, L8*, L9*, L11*, L17*, still need validation as Pseudo-capless

L5, L7, L13, L16 have internal PMIC Loads and require local CAPs.

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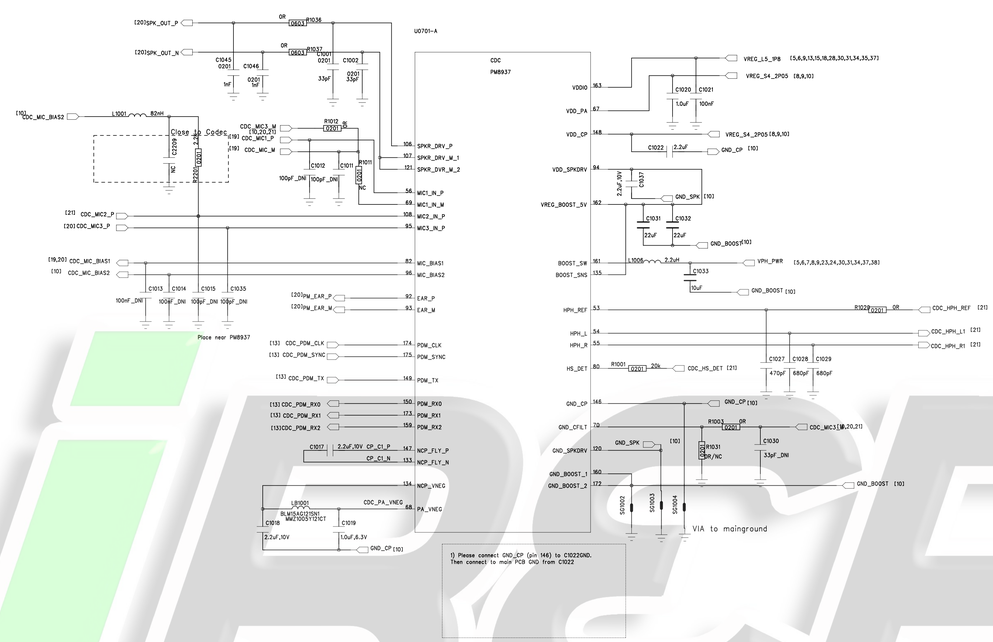

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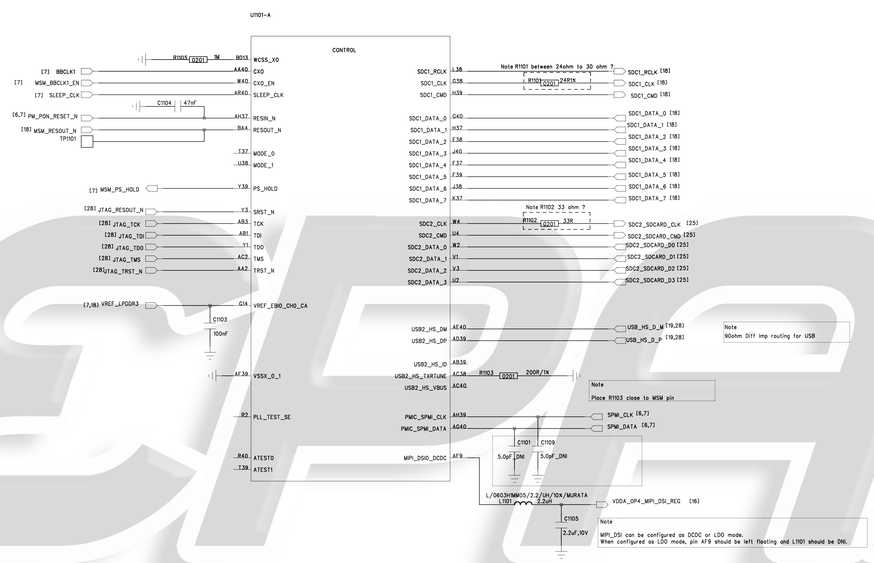

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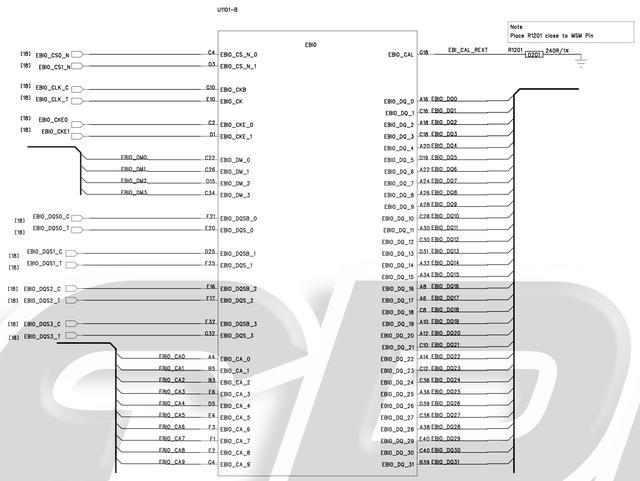

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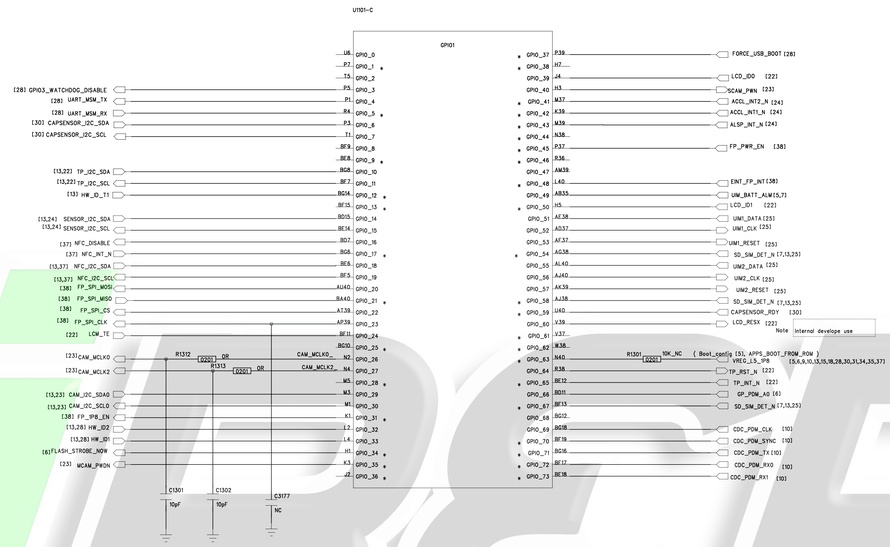
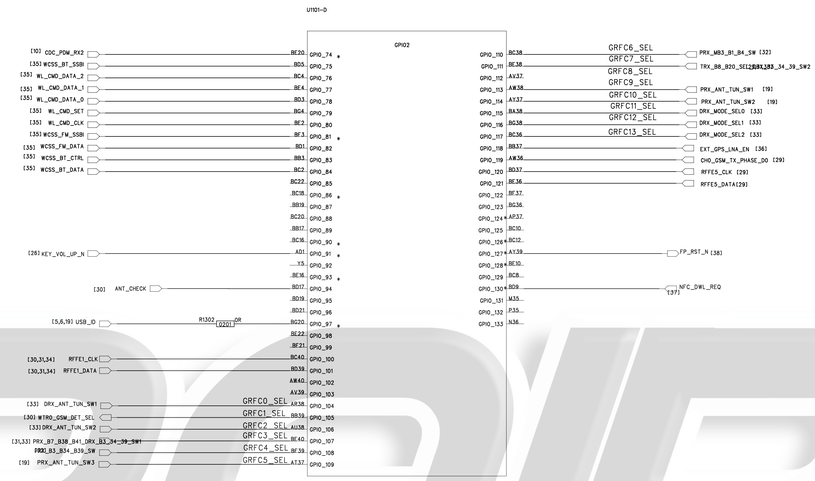
Note: Asterisks (*) indicate modem power management (MPM) wake-up pins



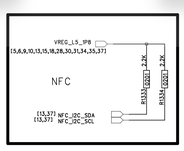


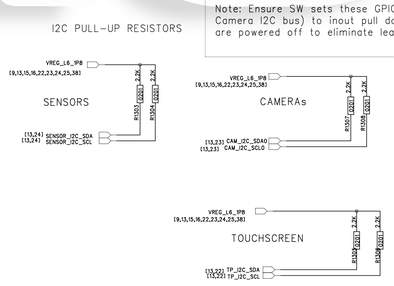
Note: Do not have pull-ups on the following GPIOs unless intend for boot or secure- boot related configurations:
GPIO_91, 107, 109

GPIO	BOOT_CONFIG
GPIO_3	BOOT_CONFIG(0)/WDOG_DISABLE
GPIO_111	BOOT_CONFIG(1)
GPIO_112	BOOT_CONFIG(2)
GPIO_88	BOOT_CONFIG(3)

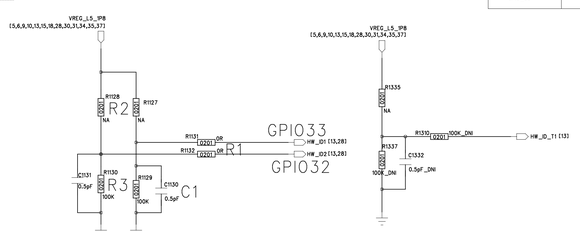
BOOT_CONFIG(3:1)	BOOT_CONFIG
0b000	SDC1 -> SDC2 -> USB2.0
0b010	SDC1
0b100	SDC2-> SDC1

Default Boot Config (0b000) is SDC1(eMMC)





Note: Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.



number of Board ID not enough !!!

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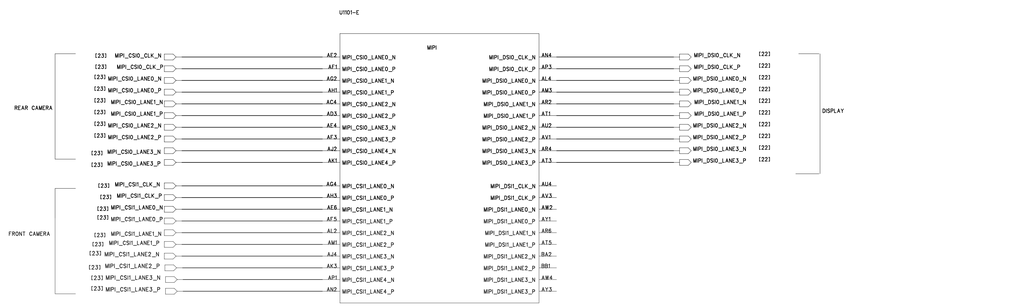
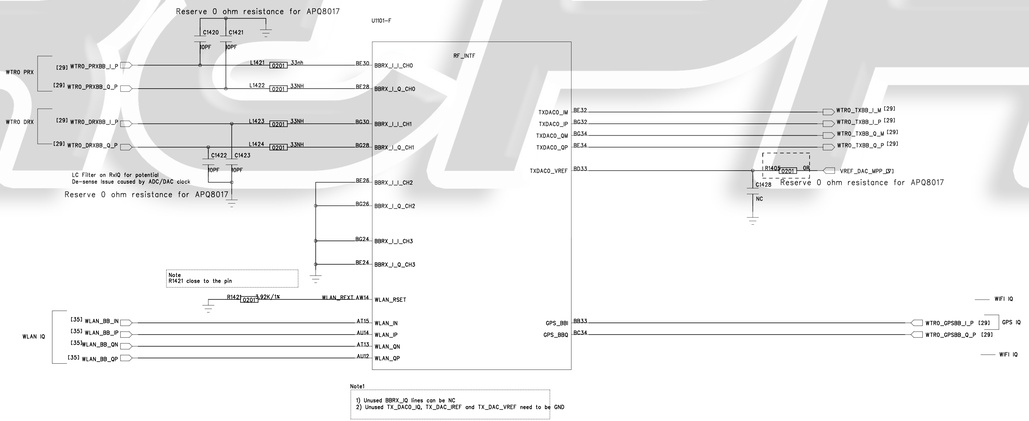


Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints.
Extreme care must be taken that no stubs are created by doing so.



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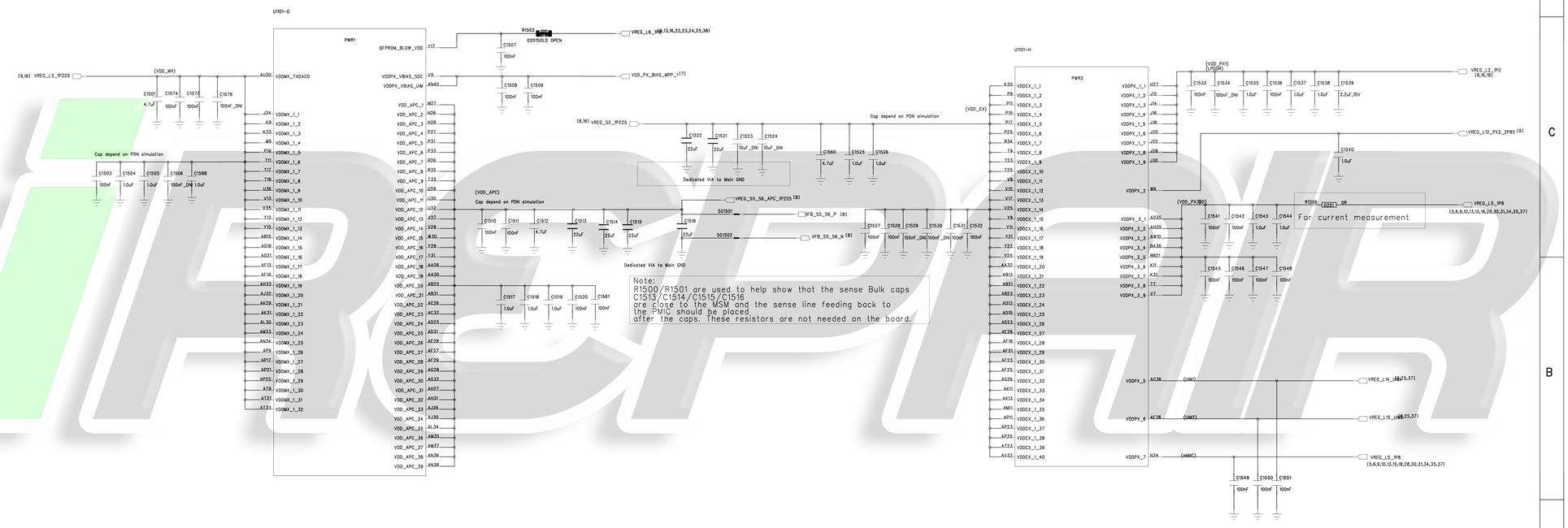

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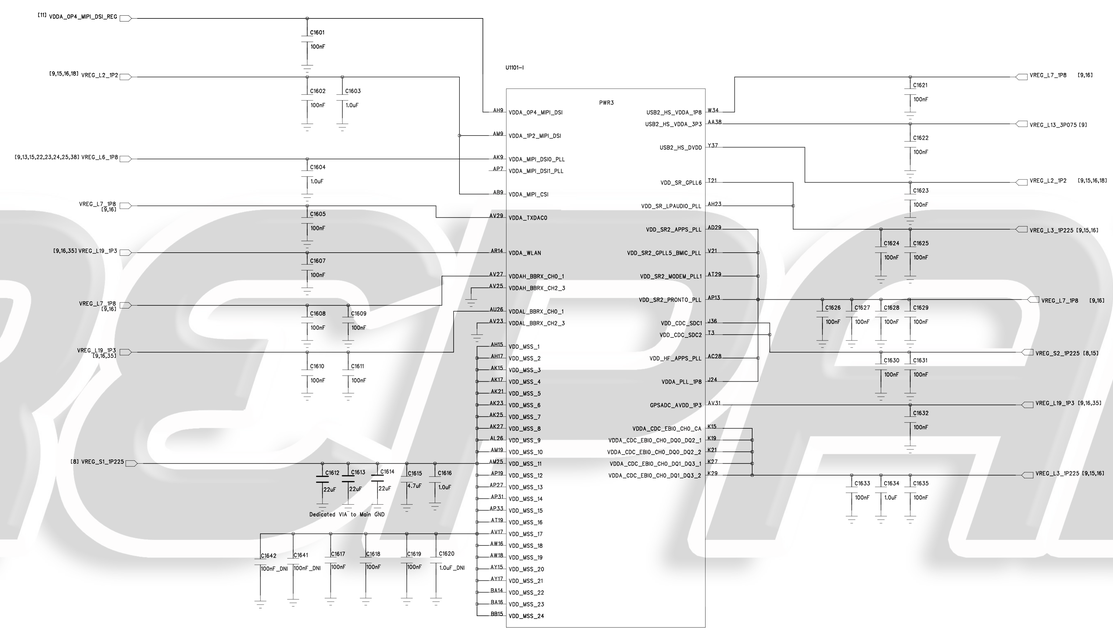

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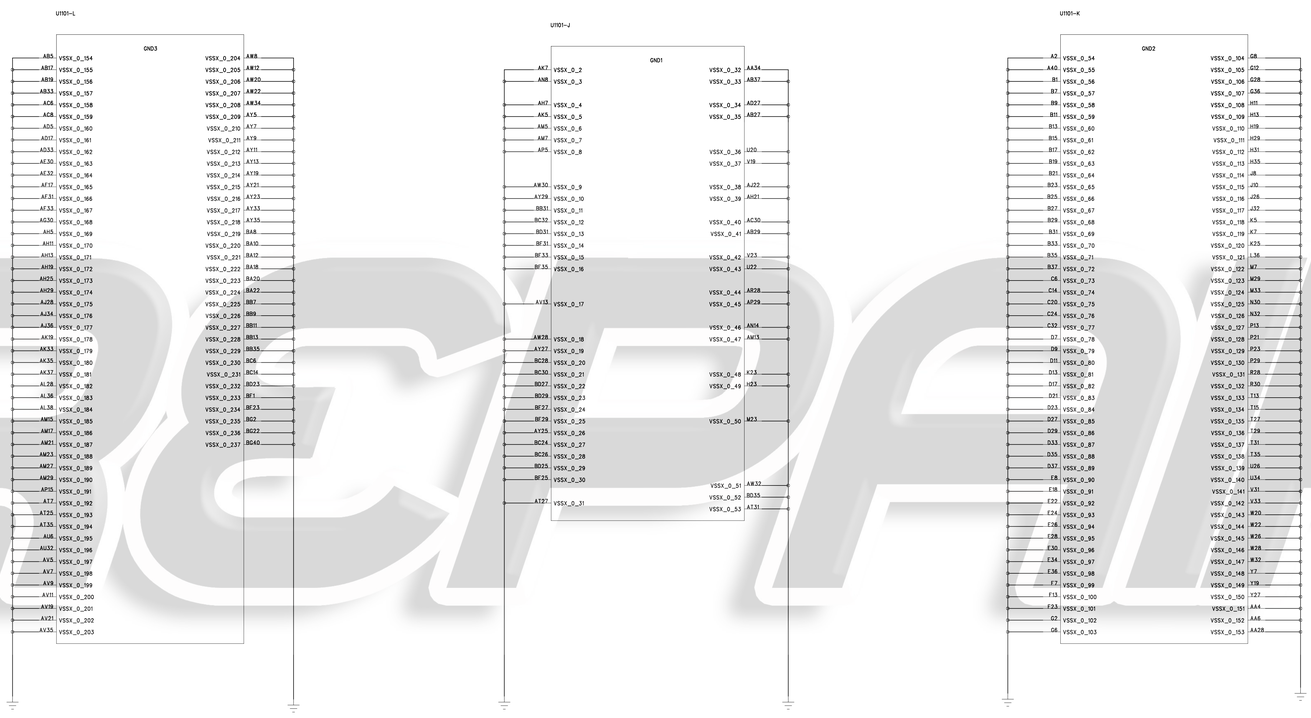

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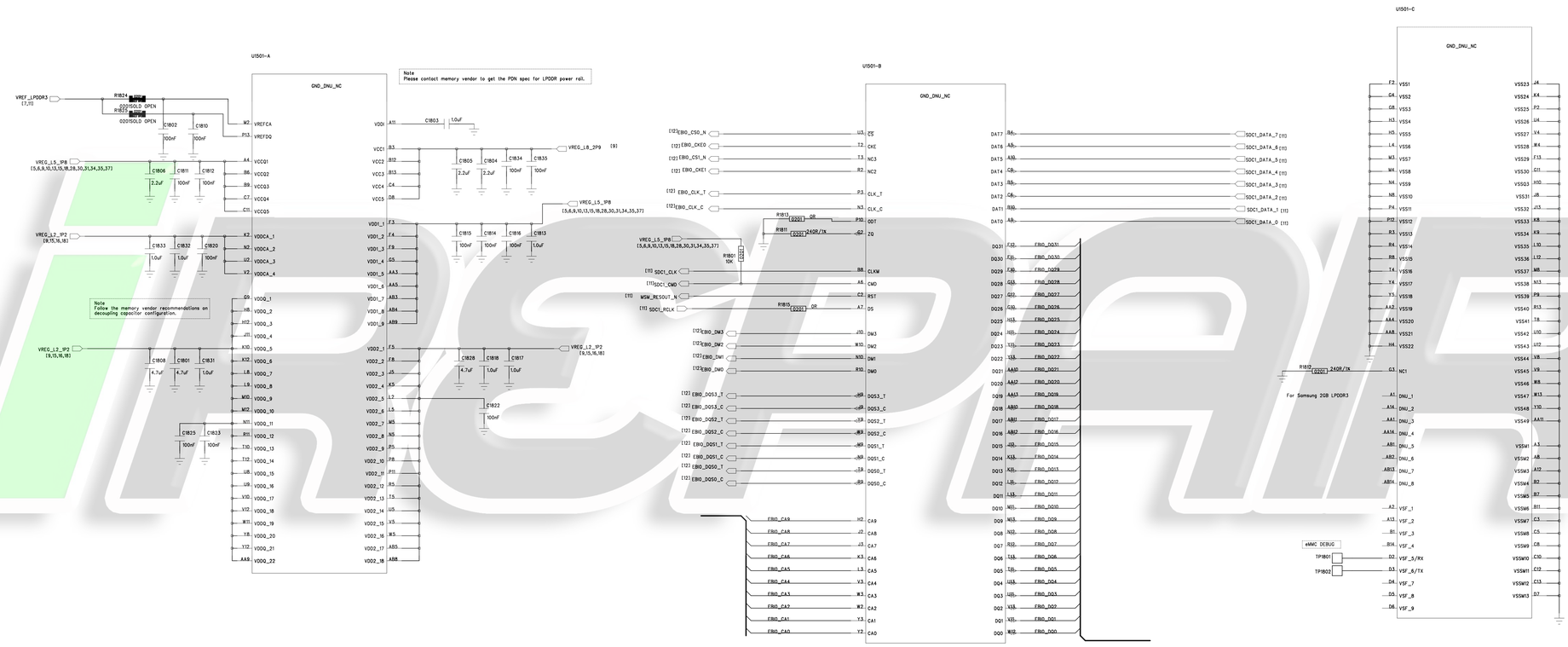

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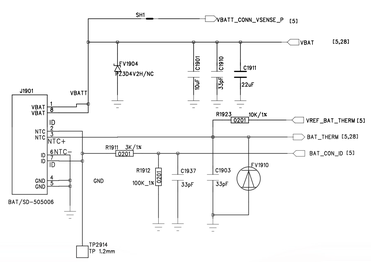

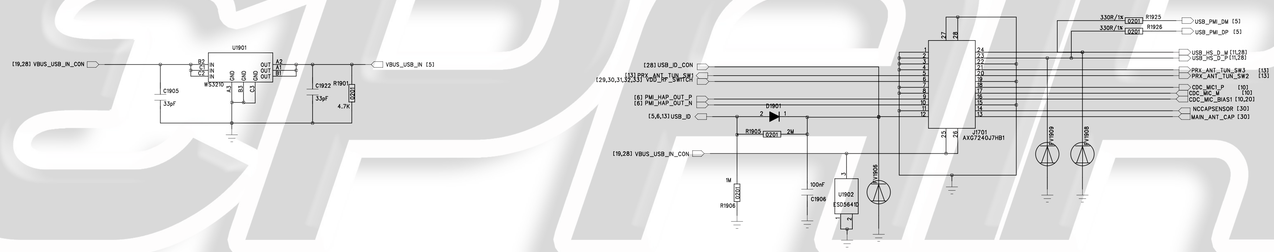

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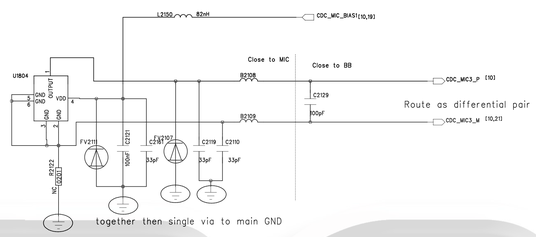

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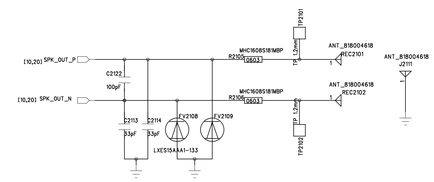

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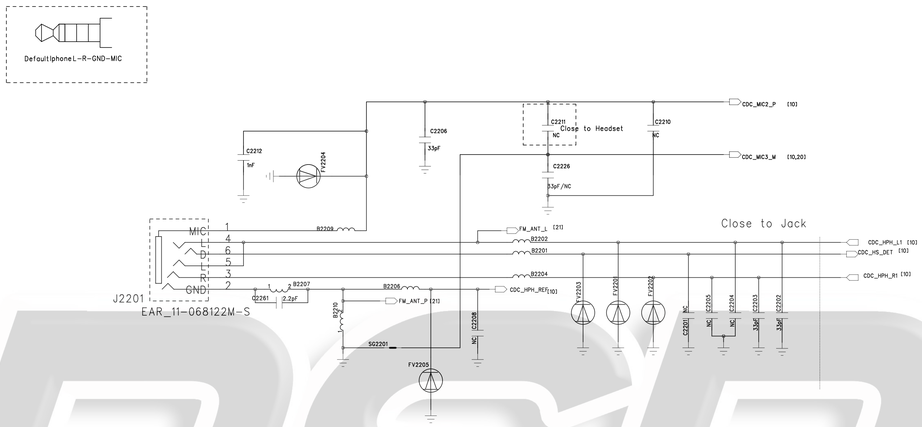
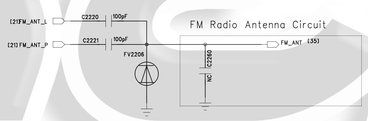
Earphone Audio





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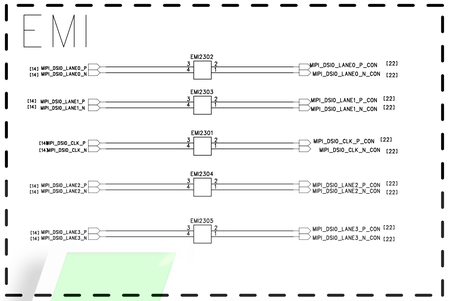
Note: If best EMI practices are followed for MIPI CSI/DSI signals, there is no need for common mode choke filters. You may choose to have placeholders for common mode depending upon your design constraints.
Extreme care must be taken that no stubs are created by doing so.

EMI



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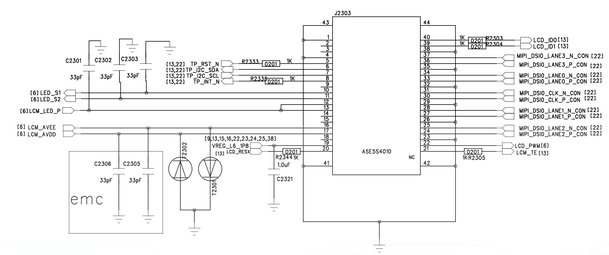

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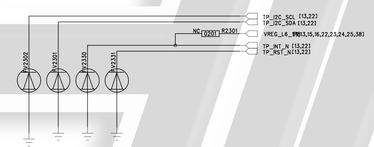

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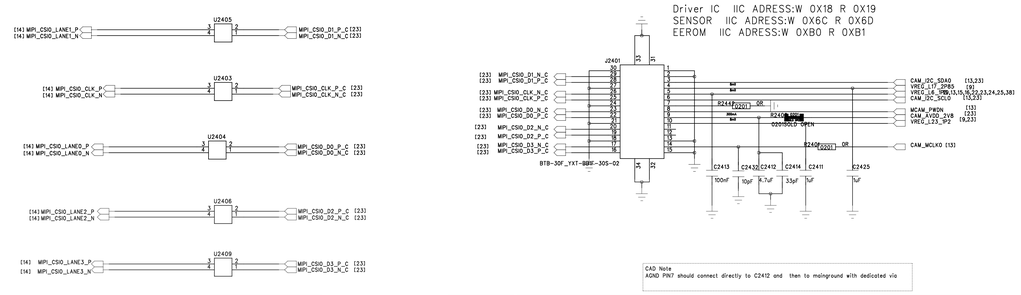
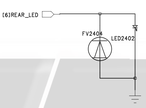
Main Camera

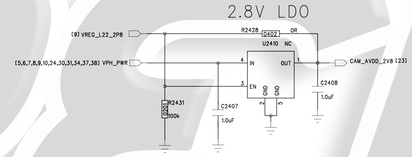


CAD Note
AGND PIN7 should connect directly to C2412 and then to mainground with dedicated via

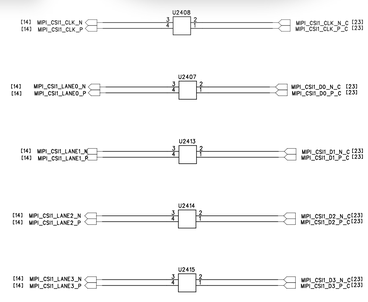
Rear Led:ELCH06-BJ4J6Z10-N0(YY)



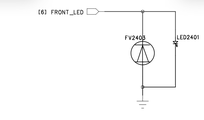
2.8V LDO



Front Camera



Front LED: LP-CSF3218001B1



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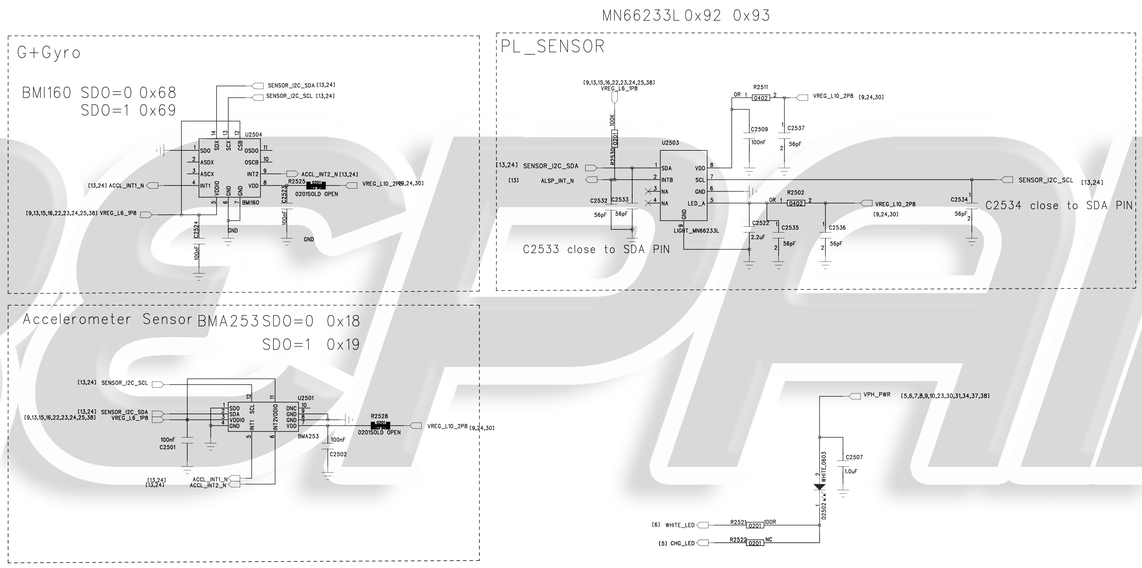

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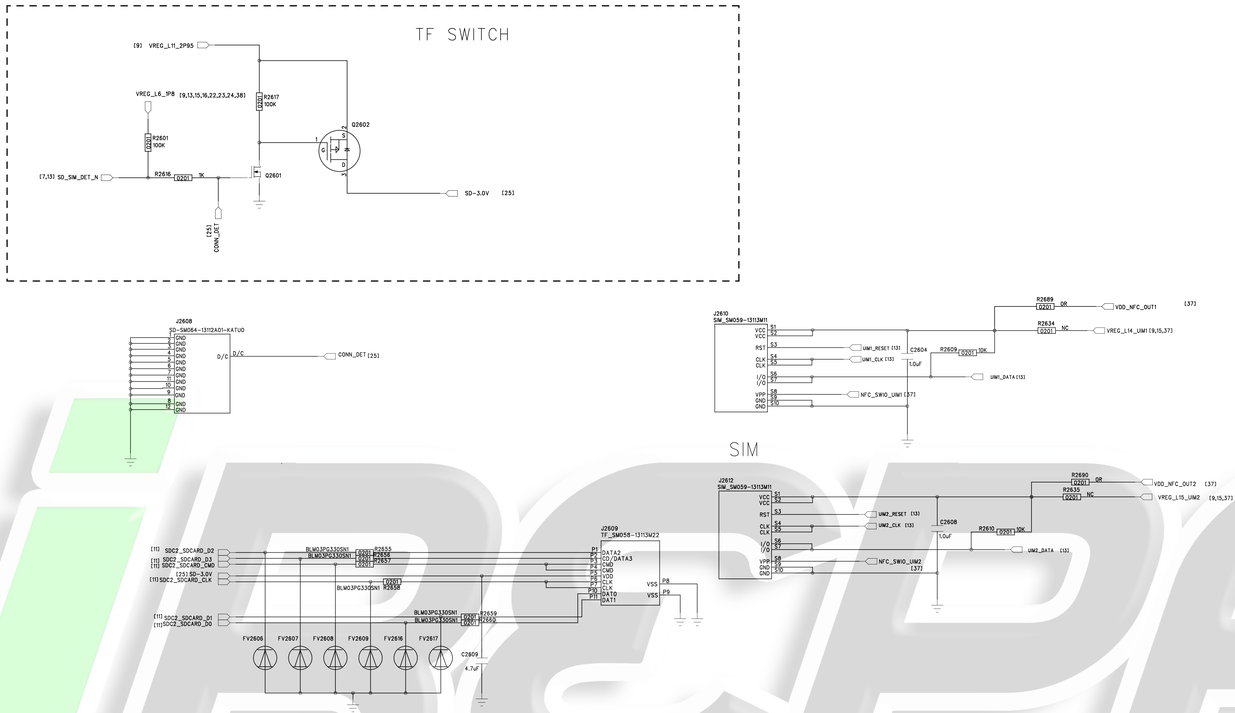

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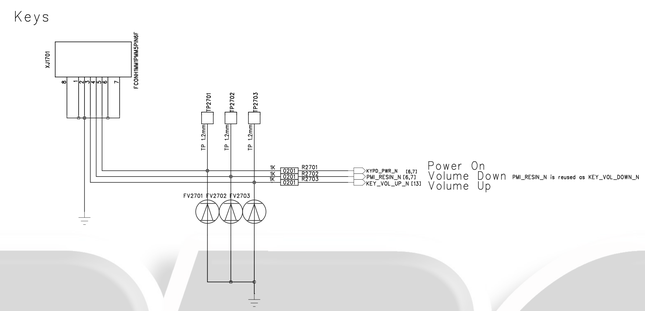

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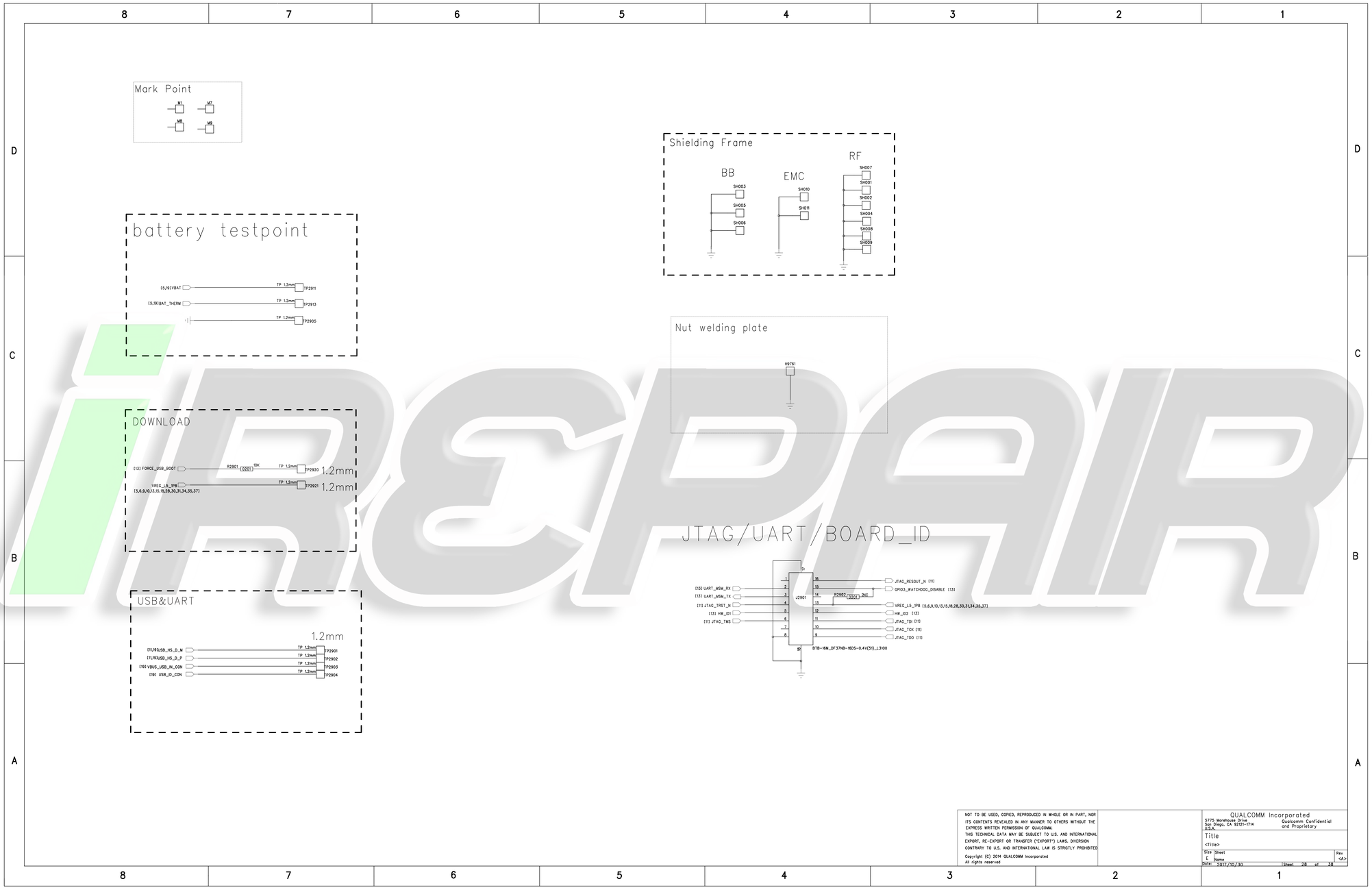

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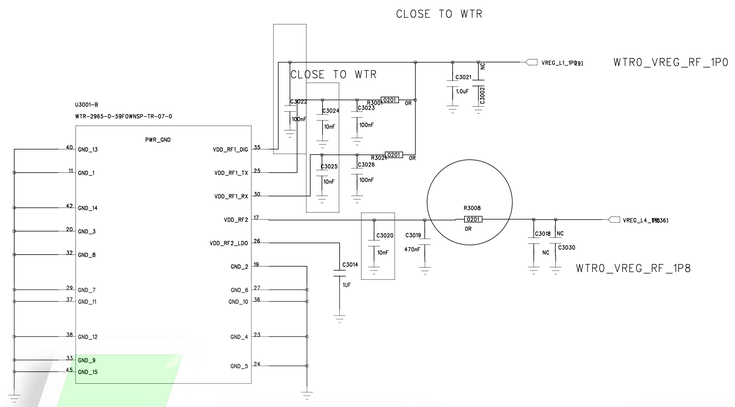

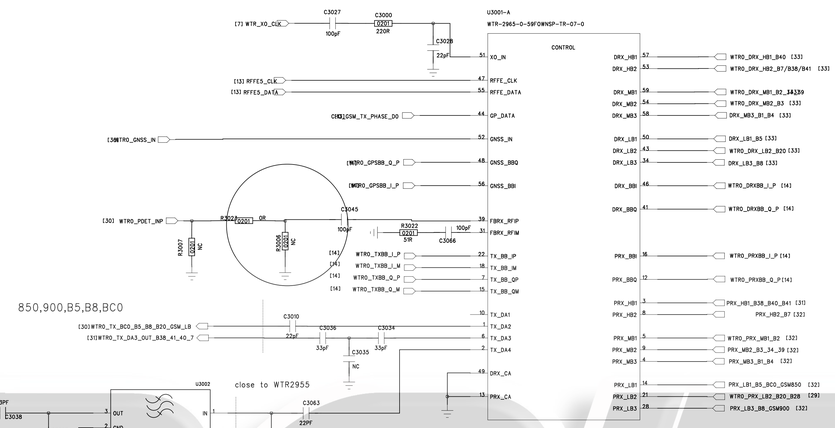

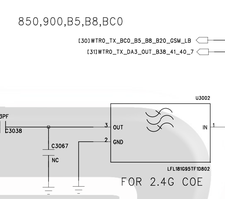

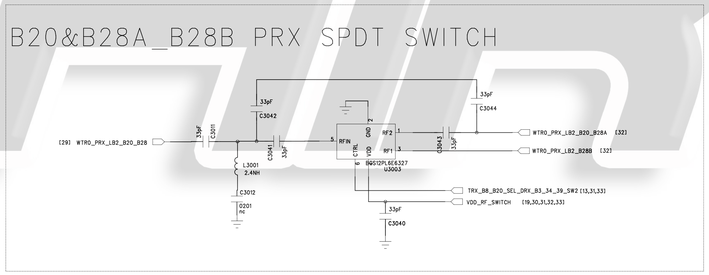

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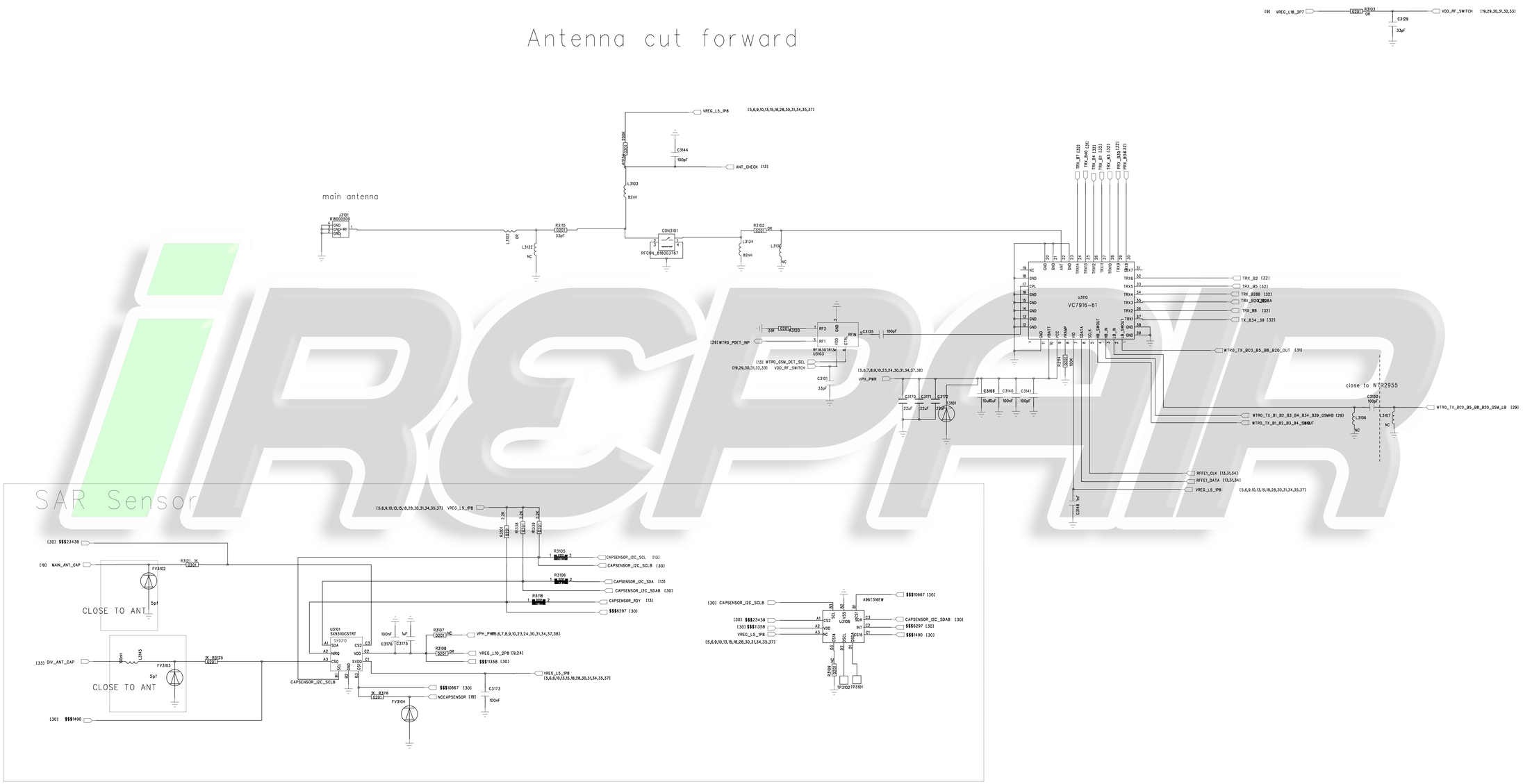

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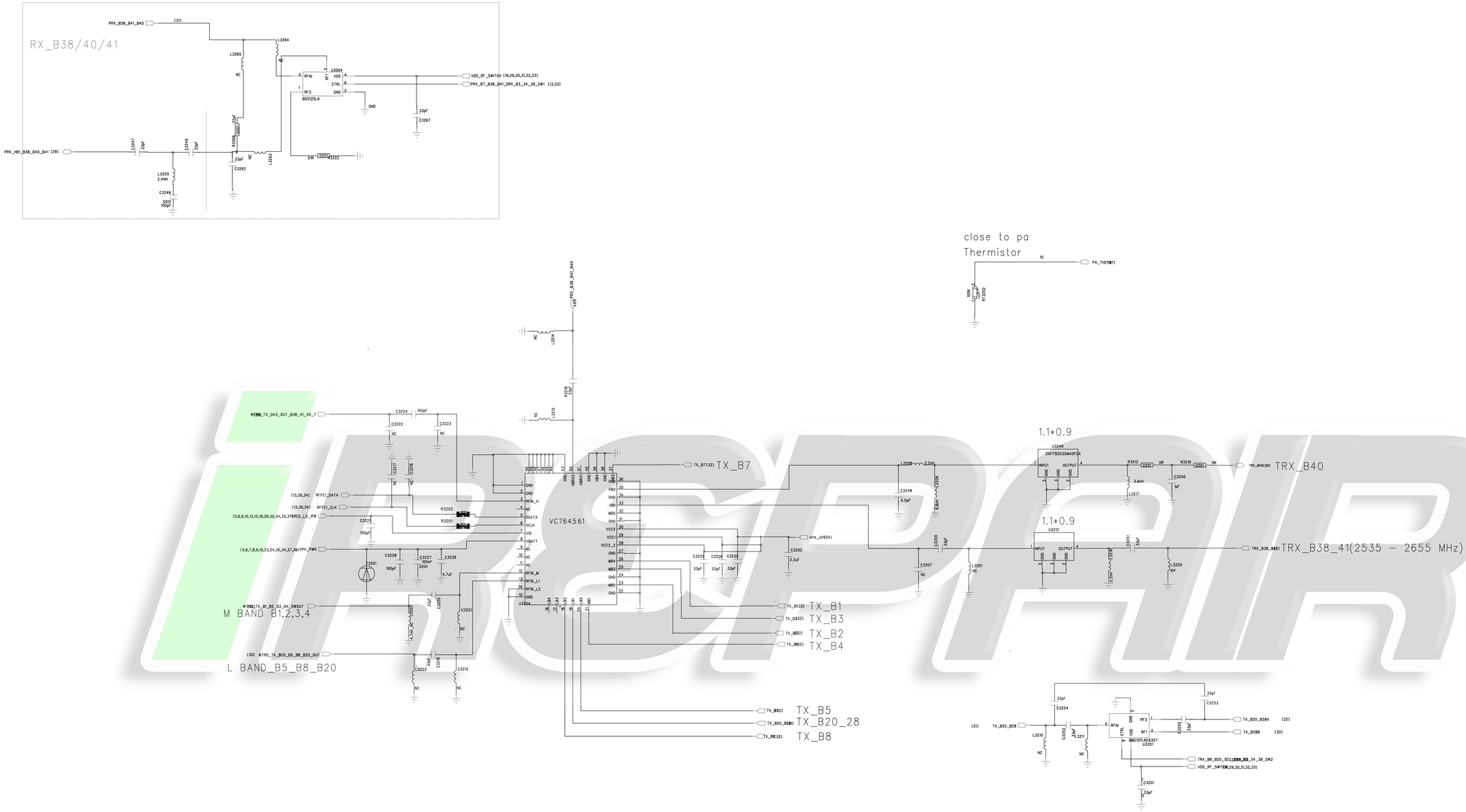

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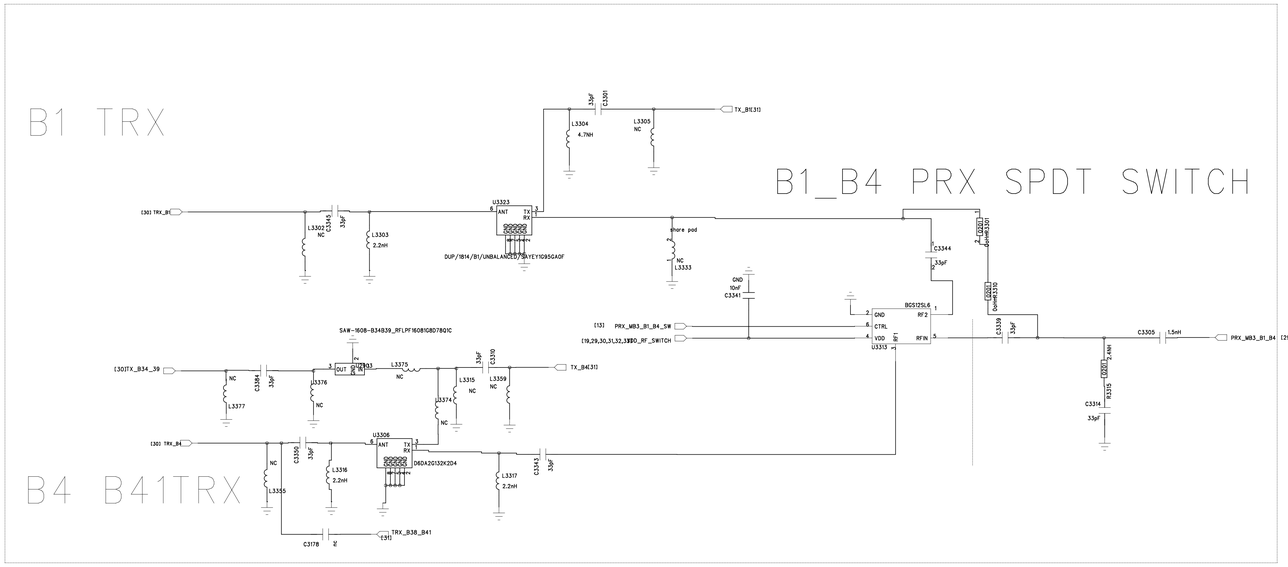

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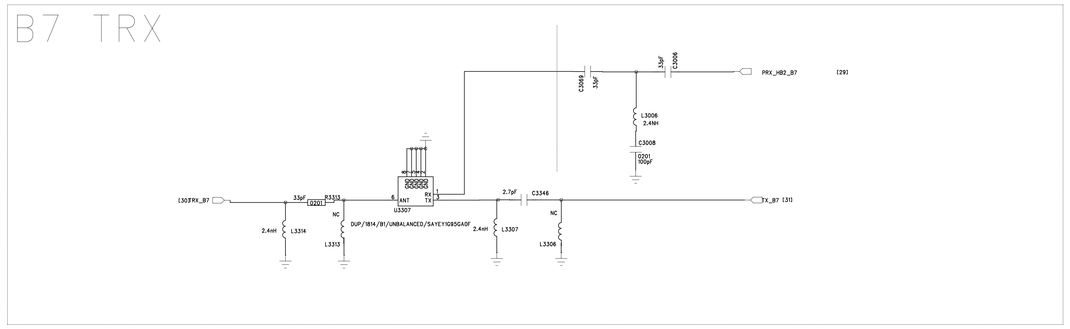

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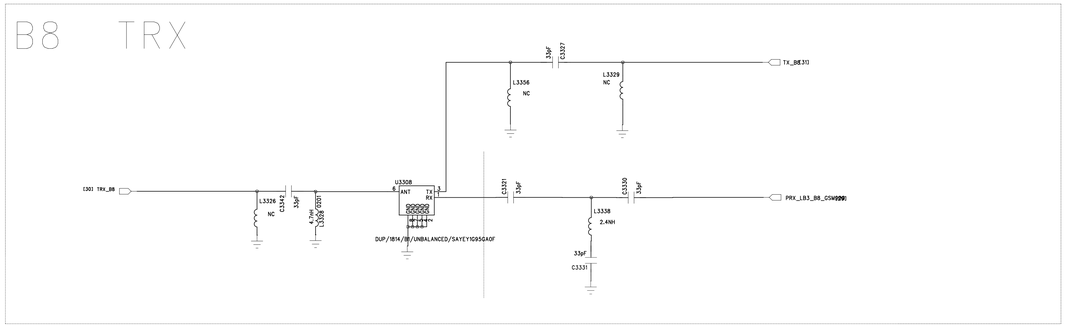

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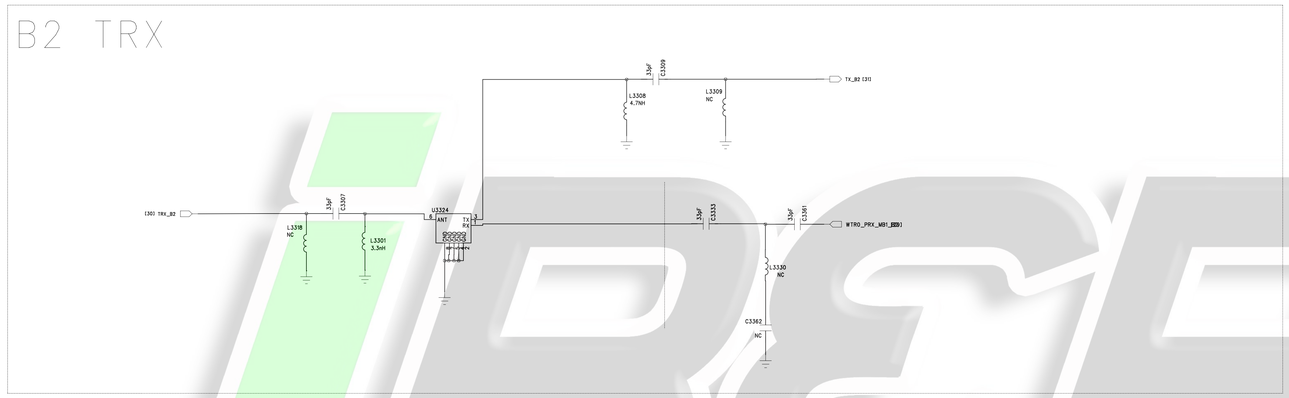

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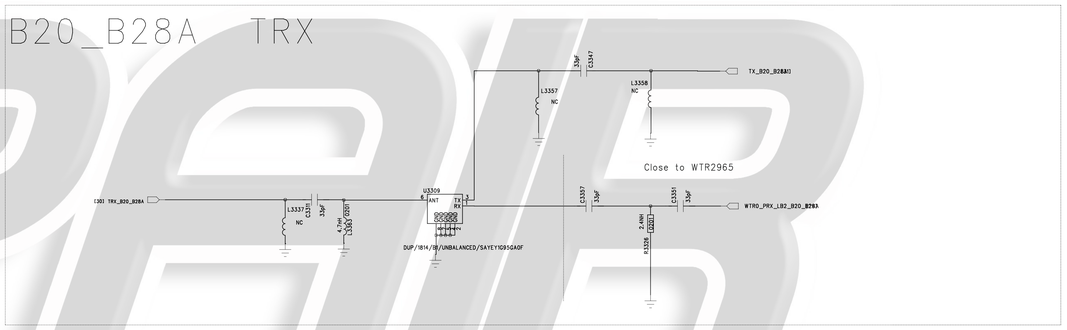

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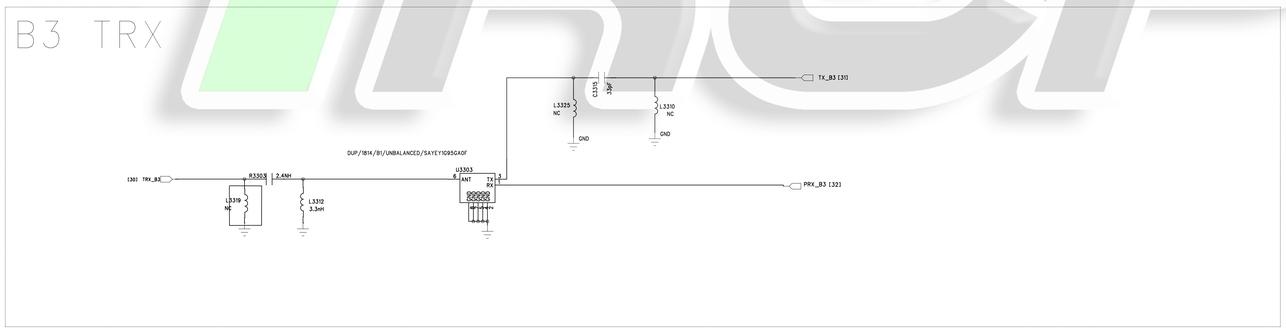

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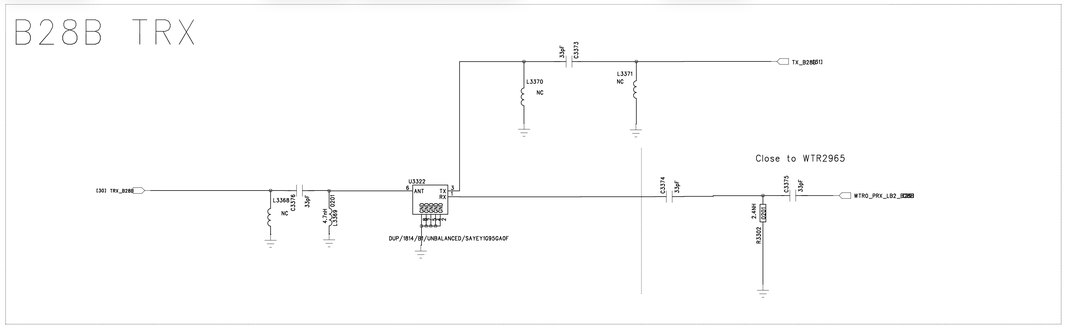

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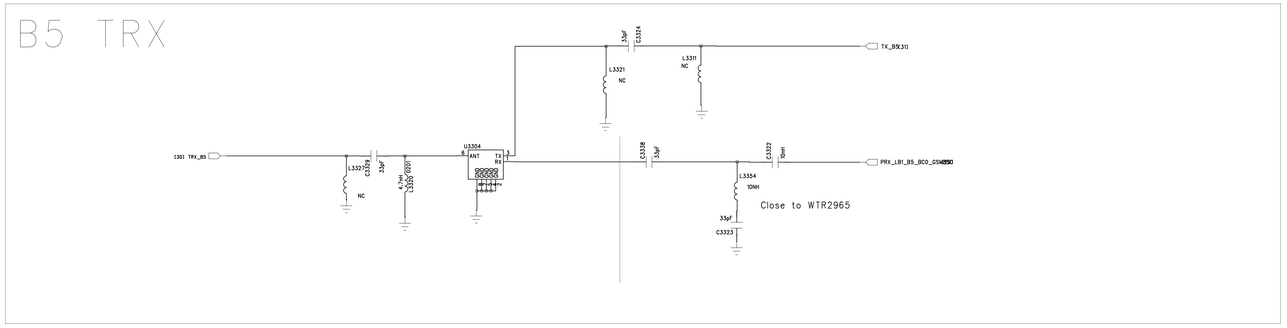

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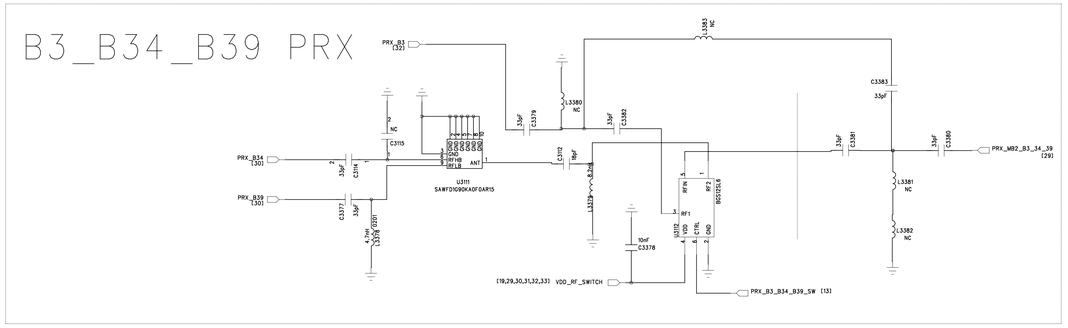

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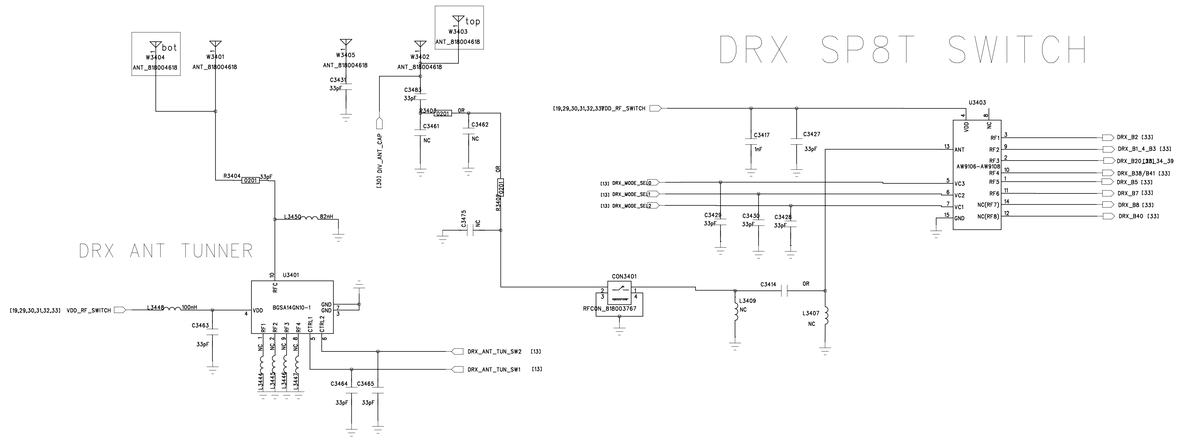

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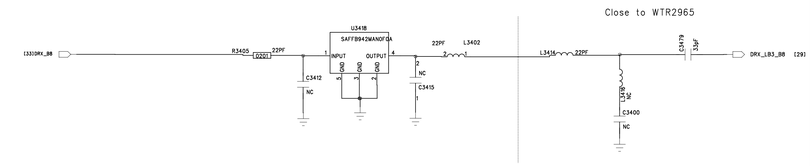

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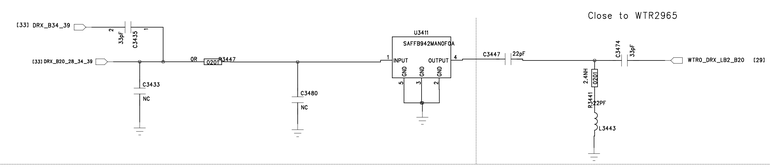

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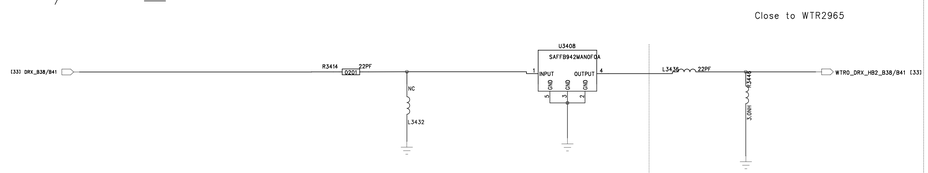

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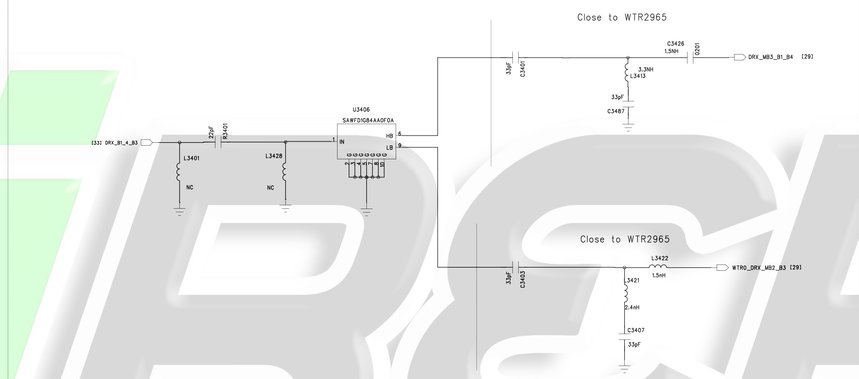

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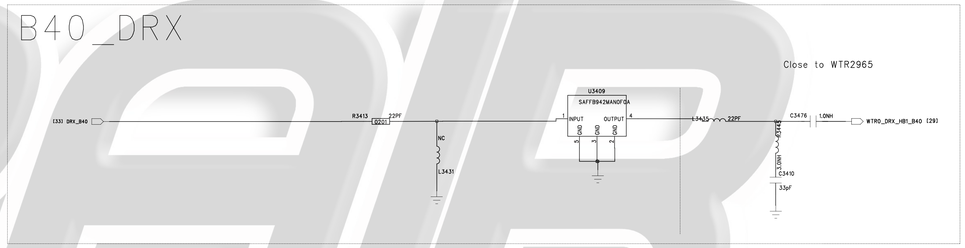

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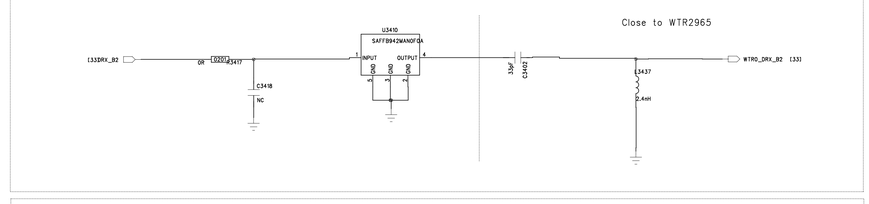

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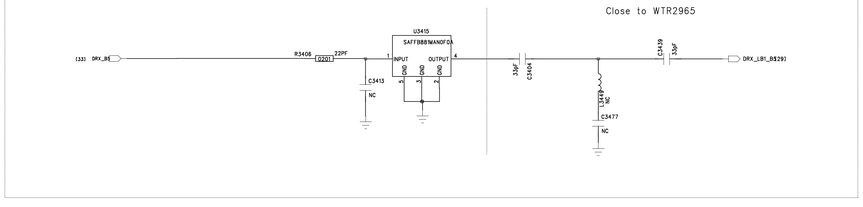

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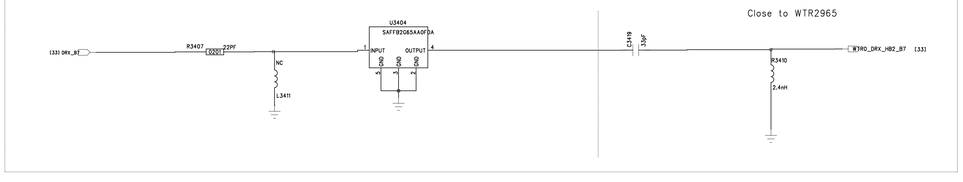

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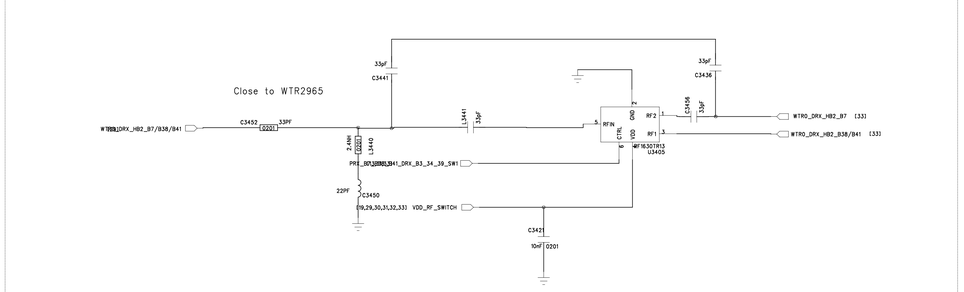

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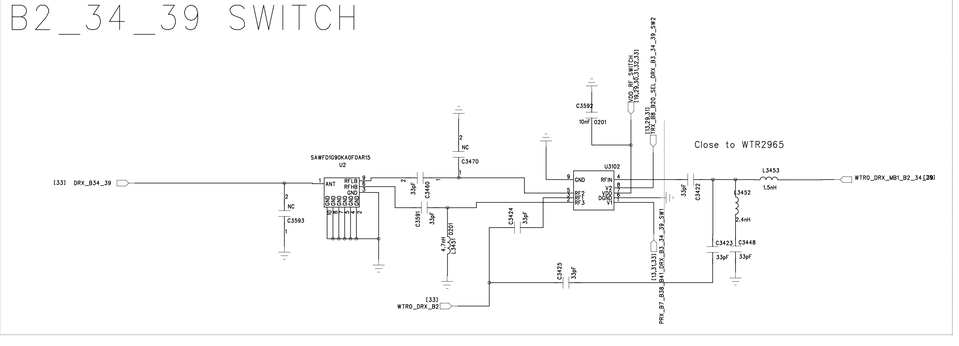

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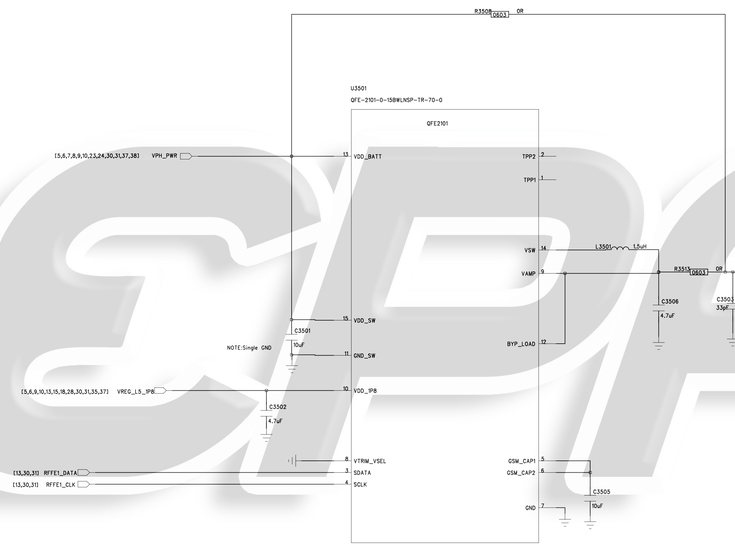
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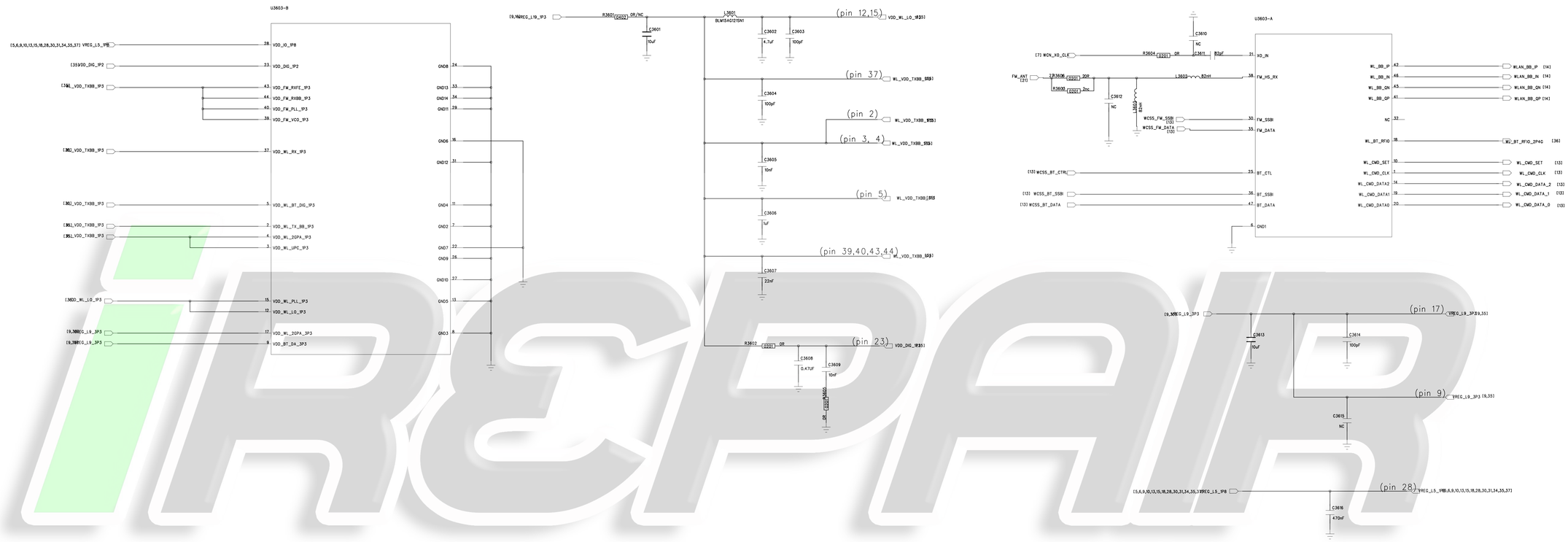
[5,6,7,8,9,10,23,24,30,31,37,38]
VPH_PWR
13
VDD_BATT
TPP2
2
TPP1
1
VSW
14
L3501
1.5uH
VAMP
9
R3515
0R0J1
0R
VPA_APT [31]
C3506
4.7uF
C3503
33pF
15
VDD_SW
C3501
10uF
NOTE:Single GND
11
GND_SW
BYP_LOAD
12
[5,6,9,10,13,15,18,28,30,31,35,37]
VREG_L5_IPB
10
VDD_IPB
C3502
4.7uF
8
VTRM_VSEL
[13,30,31]
RFFE1_DATA
3
SDATA
[13,30,31]
RFFE1_CLK
4
SCLK
GSM_CAP1
5
GSM_CAP2
6
C3505
10uF
GND
7

ANIBAL GARCIA VENEZUELA



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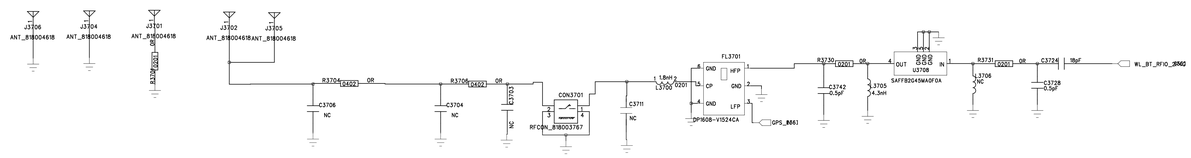

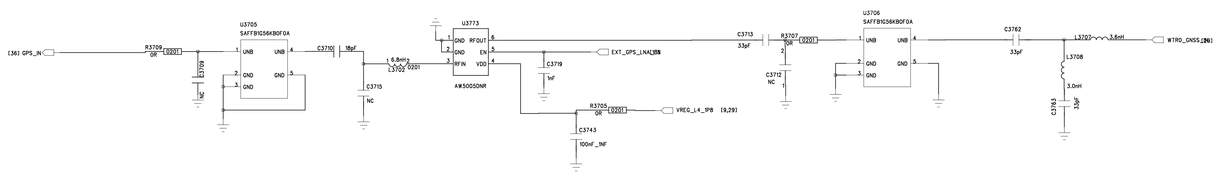

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ANIBAL GARCIA VENEZUELA

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REVISION RECORD		
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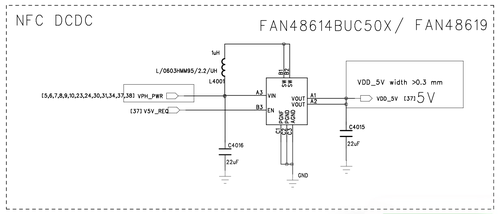

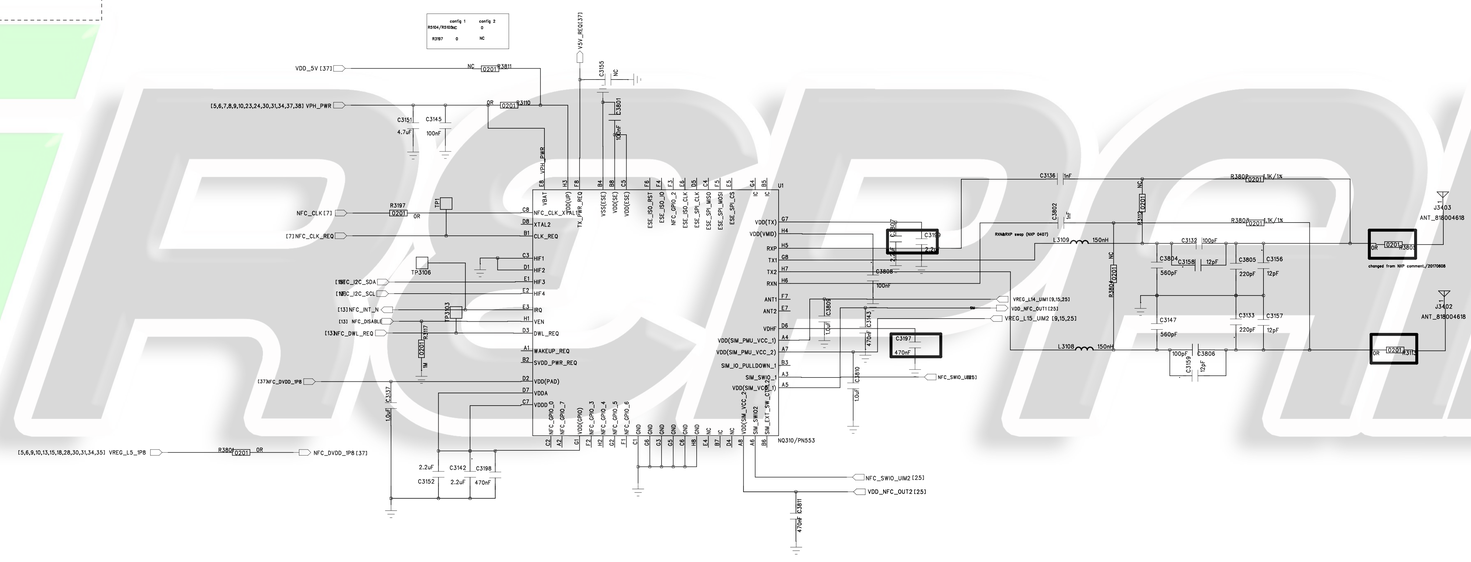

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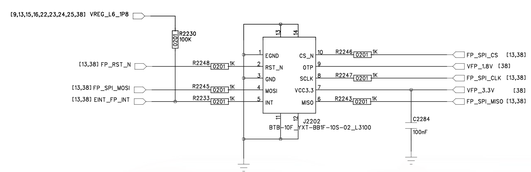
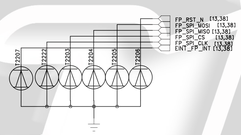
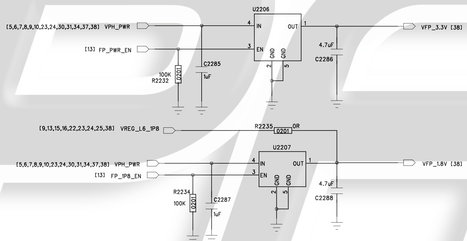
ANIBAL GARCIA VENEZUELA

REVISION	
LTR	ECO NO:







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